

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

jc997 U.S. PTO
10/046261
01/16/02

In re U.S. Patent Application of)

TAKAGAWA et al.)

Application Number: To Be Assigned)

Filed Concurrently Herewith)

For: SEMICONDUCTOR DEVICE)

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

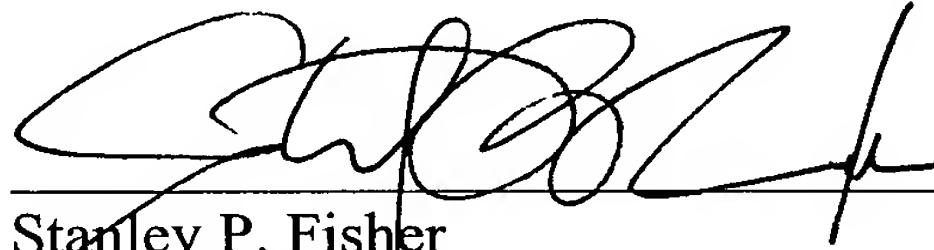
Pursuant to 37 C.F.R. §§ 1.56 and 1.97, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each U.S. and foreign patent, or each publication or portion thereof listed or herein identified, is submitted herewith.

This Information Disclosure Statement is submitted within three months of the filing date of the above-captioned U.S. patent application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Stanley P. Fisher', written over a horizontal line.

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Form PTO 1449 U.S. Department of Commerce Patent and Trademark Office Information Disclosure Statement by Applicant	ATTY. DOCKET NUMBER TSUT.0010	SERIAL NUMBER To be assigned
	APPLICANT Takagawa et al.	
	FILING DATE Concurrently Herewith	GROUP

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U.S. Patent Documents

Examiner Initial	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBC LASS	FILING DATE

Foreign Patent Documents

Exami ner Initial	DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
						YES	NO

Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)

		Nick X. Sun, Alex Q. Huang and F.C. Lee, "0.35 μ m, 43 $\mu\Omega$ cm ² power, 6 m Ω power MOSFET to power future microprocessor," 1999 IEEE, pp. 77-80
		Wharton McDaniel, "MOSFET selection is key to successful DC-DC conversion," Supplement to Electronic Design, December 6, 1999, pp. 158-165
		Edward Stanford, "Device requirements for future CPU voltage regulators," Intel. Corp., pp. 1-6

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant